# 2 Year Self-Sponsored M.Tech. (2020-21) Department of Electrical Engineering IIT Hyderabad





## **Tentative Dates**

☐ Opening of web portal for online applications : March 2021

☐ Last Date for receiving applications : April 2021

☐ Online announcement of shortlisted candidates : June/July 2021

☐ Written test and interview : July 2021

(Please check IITH admissions page for the latest schedule)

# **Program Overview**

Opportunity for full-time MTech program
Selection via written test and interview.
Duration of the program: Four semesters (2-year program)
No difference in curriculum and degree to be awarded between MHRD funded and
Self-sponsored MTech candidates
Eligible for On-Campus Placement on successful completion of the program
Non subsidised Program - No fellowship (even for GATE qualified students)
Open for B.Tech/B.E degree holders having a first-class
GATE qualification is not mandatory
Admission based on a thorough scrutiny process comprising written test
and interview.
Total Credits to be completed: 48 – 52 Credits
Non-Residential Program - Hostel accommodation not guaranteed
Consideration for hostel accommodation (chargeable) based on room availability
Conversion to M.Tech (TA-MHRD)/ M.Tech (RA-MHRD) or vice versa is not
allowed.

## Available Specializations and Eligibility to apply

➤ BE/B.Tech. with first-class (60% and above) and below disciplines.

M.Tech Specialization	BE/B.Tech Discipline	Written-Exam Syllabus		
Communication & signal processing	EE/ECE/MC or equivalent	EC Gate http://gate.iitd.ac.in/Syllabus/EC.pdf		
Microelectronics and VLSI	EE/ECE/EI or equivalent	EC Gate http://gate.iitd.ac.in/Syllabus/EC.pdf		
Power Electronics & Power Systems	EE or EEE or equivalent	EE Gate http://gate.iitd.ac.in/Syllabus/EE.pdf		
Systems and Control	EE/EI or equivalent	EE Gate http://gate.iitd.ac.in/Syllabus/EE.pdf		

## **Indian Institute of Technology-Hyderabad**

IIT Hyderabad is one of the fastest-growing institutes in India and one among the 2nd generation of IITs started by the Govt. of India in 2008. As of date, IITH offers 10 B.Tech programs, 21 M.Tech programs, 3 M.Sc programs, 5 M.Phil programs, 1 M.Des program and Ph.D. programs in all branches of engineering, science, liberal arts and design. The very foundation of IIT Hyderabad is based on research and innovation. The vibrant research culture is evident from the number of patents and publications that IITH is generating. The institute has about 230 faculty and 2,500 students. Institute is offering a 'Self Sponsored M.Tech.' program to give the opportunity to deserving candidates via written test and interview.

## **Electrical Engineering Department Overview**

The Department of Electrical Engineering is the largest department at IIT Hyderabad with 192 B-Tech students, 122 M-Tech students and 162 PhD scholars. We have a team of 32 dedicated and dynamic faculty members (28 fulltime + 4 visiting). Electrical Engineering department is known for its rich industrial collaborations and sponsor projects, across all departments in IIT Hyderabad.

Please visit <a href="https://ee.iith.ac.in">https://ee.iith.ac.in</a> to know more about research facilities, laboratories, MTech curriculum, and research area of various faculties.

## **Self-Sponsored M.Tech. Program**

In view of the high demand in industries and research organizations for the highly skilled professionals and experts in specific domains, IITH had started an All course MTech program (ACM). After the success of ACM program, the institute has further revamped it to Self-sponsored MTech program in 2020 to bring it at par with the regular MTech Program, to benefit the students.

## M.Tech. Specializations

For the 2-year Self-Sponsored M.Tech program, candidates can select one of four specializations at the time of application:

#### **Communications and Signal Processing**

Research areas: 5G, mmWave and LiFi communications, 3D immersive display, AI and ML, Internet-of-things (IoT) and cyber physical systems, Information theory and coding, Performance analysis, Resource allocation and Game theory, Speech and multimedia signal processing, Security and privacy, UAV based sensing

#### Microelectronics and VLSI

Research areas: VLSI/ULSI IC and system design, Nanoelectronics, Nano bio sensors, gas sensors, Nanophotonics, metamaterials, optoelectronic devices, 3DIC, MEMS-ASIC integration, Flexible electronics, Embedded systems, Analog, digital and mixed signal VLSI, Energy harvesting, ICs for wireless communication, Integrated microelectronic devices

#### **Power Electronics and Power Systems**

Microgrids, Renewable energy systems, Multilevel inverters and drives, Power quality, Switched mode power conversion, Converter design for grid connected renewable energy, Power system stability, Power system protection, Smart grids, Wide area monitoring and control

#### **Systems and Control**

Pattern matching and data mining, Big data analytics, Condition monitoring, Advanced/statistical control, Systems biology

### Fee Structure

- Refer <a href="https://www.iith.ac.in/academics/fee-structure/">https://www.iith.ac.in/academics/fee-structure/</a> for program fee structure.
- Hostel accommodation not guaranteed. Hostel accommodation and Mess facility not included in fee structure and chargeable as per norms, subject to the availability.

## Opportunities after M.Tech.

• All course M.Tech. (old version self-sponsored MTech) Placement:

Year	Cate- gory	Total Students	Registered For place- ments	Placed Including PPO	Double Offer	Higher Studies	Non- Placed	Success %
19-20	ACM	7	7	7	0	0	0	100%
18-19	ACM	11	11	8	3	3	0	100%

International offers: TSMC TAIWAN, Rakuten JAPAN, Denso JAPAN, Toyota(TRIAD) JAPAN

National offers: Intel, TCS IOT, Redpine, Xilinx, GE, TCS R&D, Atkins

#### Note

The department reserves the right to set any cutoff for the shortlisting of M.Tech applications. In addition, the department has all the rights to withdraw seats and not

select anybody if no appropriate candidates are found. Mere eligibility does not imply that the candidate would be necessarily shortlisted for the written test/interview.								